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August 2, 2007

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450


Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,009,300
Issued: March 7, 2006
For: **LOW PROFILE STACKED MULTI-CHIP
PACKAGE AND METHOD OF FORMING
SAME**
Inventor: Gacusan
Our File No. 42390.P12134d

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Edwin H. Taylor
Reg. No. 25,129

EHT/sn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,009,300
DATED : March 7, 2006
INVENTOR(S) : Gacusan

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 6, at line 1, delete "dielectric second" and insert --second dielectric--.

MAILING ADDRESS OF SENDER
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PATENT NO. 7,009,300

Certificate of Correction (PTO Form 1050)-Amended